

CLASS SYLLABUS

CETT 1403 DC Circuits

16223 Lec 005

Schedule	Fall 2010- Monday and Wednesday – 4:15 pm to 6:55 pm
Instructor	Don Holden - 223-6225 - donnjudyholden@gmail.com
Web site	http://www.austincc.edu/dholden The web site will be a work in progress for DC Circuits
Office Hours	Monday and Wednesday 3:30 to 4:15, and by appointment.
Course Description	A study of the fundamentals of direct current including Ohm's law, Kirchhoff's laws and circuit analysis techniques..
Textbooks	<u>"Introductory Circuit Analysis"</u> , by Robert Boylestad (twelfth edition).
Lab Kits	Lab kits are available at the book store.
Course Format	The course will be a combination of lectures, labs, and homework problem working sessions.
Course Rationale	This course is the first introduction to all the degree plans in the Electronics and Advanced Technologies department. Students will analytically solve series, parallel, and series/parallel circuits. Many of these circuits will be breadboarded in the lab to confirm the analytical solutions.
Course Objectives/Outcomes	End-of-Course Outcomes: Apply safety techniques while working on and troubleshooting various circuits and components; interpret color codes and other descriptors used in electronics; identify various sources of electricity in DC circuits; interpret characteristics of voltage, current, resistance, and power in DC circuits; measure voltage, current, and resistance in DC circuits using measuring devices.
Grading	Your grade is based upon four comprehensive examinations, lab work, and exercises (homework). The weights of these activities in determining your final grade are: Exams 60%, Exercises 20 %, Lab 20%. Standard letter grades will be used.
Class Policies	(A) Acts prohibited by the college for which discipline may be administered include scholastic dishonesty, including but not limited to cheating on an exam or quiz, plagiarizing, and unauthorized collaboration with another in preparing outside work. Academic work submitted by students shall be the result of their thought, research or self-expression. Academic work is defined as, but not limited to, test, quizzes, whether taken electronically or on paper; projects, either individual or group, classroom presentations, and homework. The penalty for any violation of this policy is withdrawal from the course. (B) Each ACC campus offers support services for students with documented physical or psychological disabilities. Students with disabilities must request

reasonable accommodations through the Office for Students with Disabilities on the campus where they expect to take the majority of their classes. Students are encouraged to do this three weeks before the start of the semester.

(C) ATTENDANCE IS MANDATORY. If you need to miss class due to your job requirements, check with me first. If you miss very many classes, without pre-arrangement, I will probably drop you. If you get hopelessly behind, it is **your** responsibility to withdraw on or before October 18, 2010.

(D) Each student is strongly encouraged to participate in class - questions are especially appreciated. Just about any topic in electronics is fair game, if time permits. Students will sometimes disagree with each other, and with the instructor. It is expected that faculty and students will respect the views of others when expressed in classroom discussions. The instructor will not, however, indicate agreement with a student when he feels that the student is technically incorrect. He will instead, respectfully agree to disagree.

Students in the electronics department who are seeking a degree or certificate in any specialty, should visit with the electronics student advisor, Vidal Almanza, (RVS Campus, Bldg. G, Student Services, 512-223-6404; vman@austincc.edu) if they haven't already for a degree audit.

All Electronics students must check their ACC gmail regularly throughout the semester. We will be sending pertinent information about scholarships, the course scheduling needs survey, job opportunities, MSDNAA free student software program, career fairs, special events, and etc. through the student gmail system.

All degree and certificate seeking students should declare their major at the Admissions & Records Office if they have not done so already

Course Outline and Tentative Schedule

Week 1	8/23	Chapter 1	Introduction
Week 2	8/30	Chapter 2	Voltage and Current
Week 3	9/6		
Week 4	9/13	Chapter 3	Resistance, Exam 1
Week 5	9/20		
Week 6	9/27	Chapter 4	Ohm's Law, Power, and Energy
Week 7	10/4	Chapter 5	Series DC Circuits
Week 8	10/11		Exam 2
Week 9	10/18	Chapter 6	Parallel DC Circuits
Week 10	10/25		
Week 11	11/1	Chapter 7	Series – Parallel DC Circuits
Week 12	11/8		Exam 3
Week 13	11/15	Chapter 8, Chapter 9	Methods of Analysis, Network Theorems
Week 14	11/22	Chapter 10	Capacitors
Week 15	11/29	Chapter 11	Inductors
Week 16	12/6	Chapter 12	Magnetic Circuits, Final Exam